

FIG.



FIG. 2A

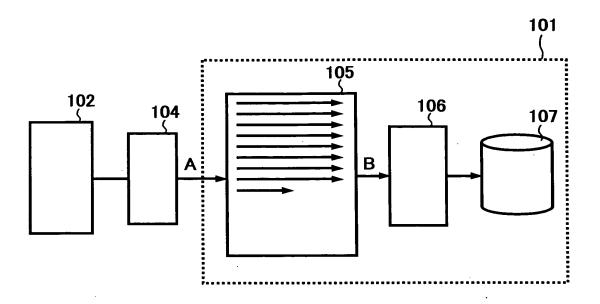
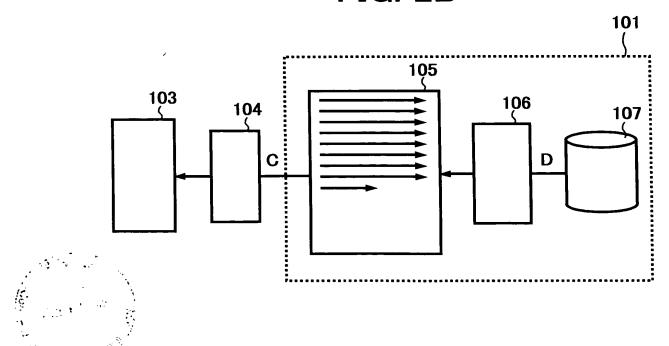


FIG. 2B



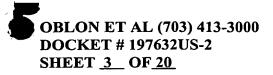




FIG. 3

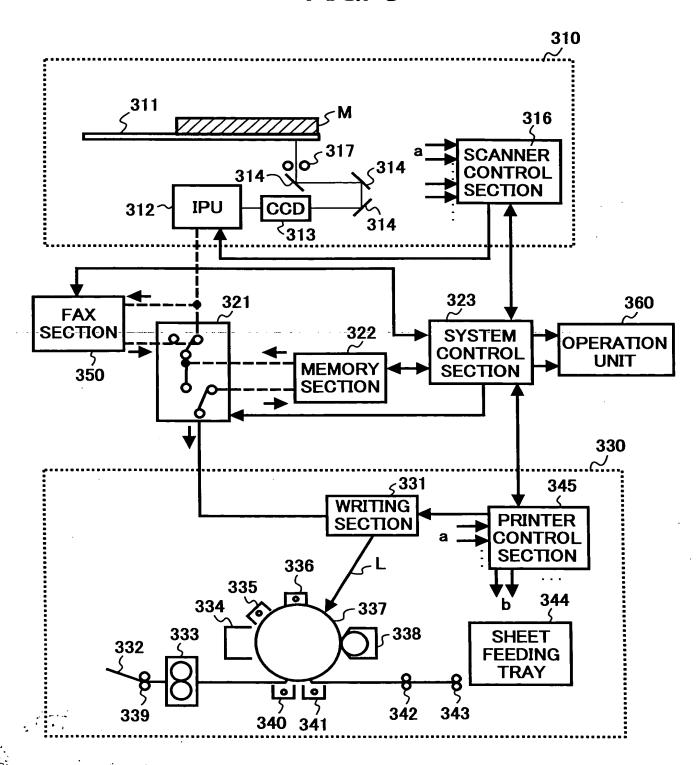
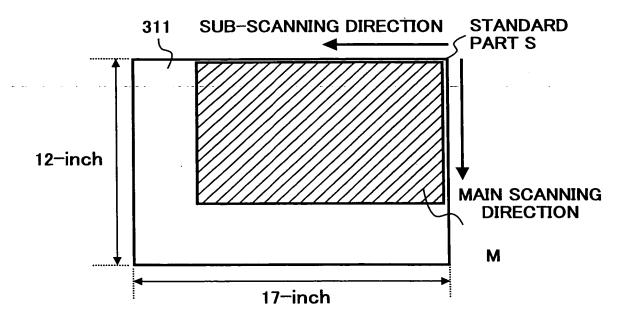
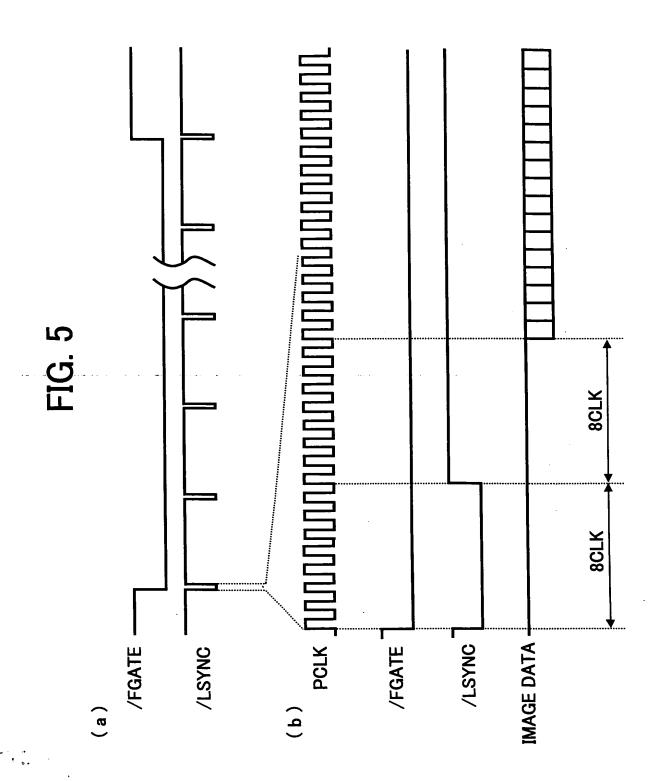


FIG. 4



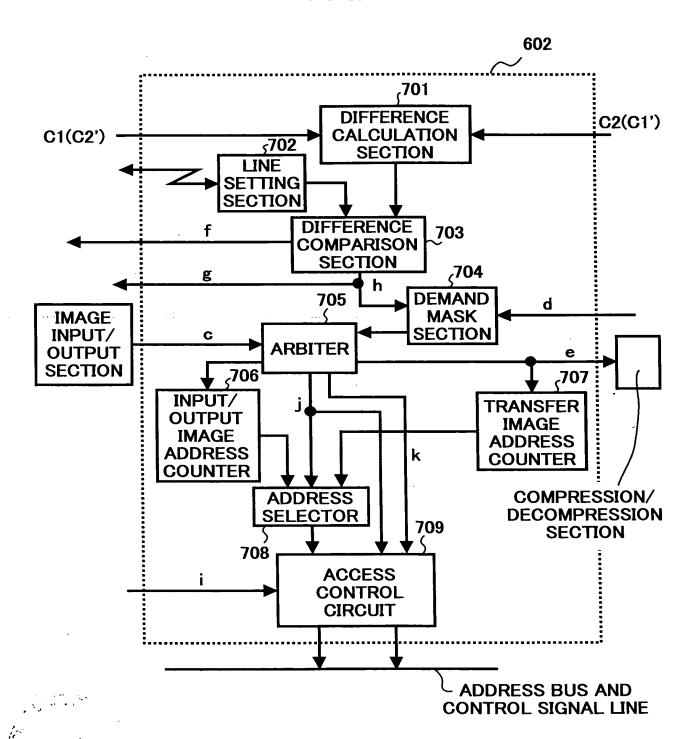




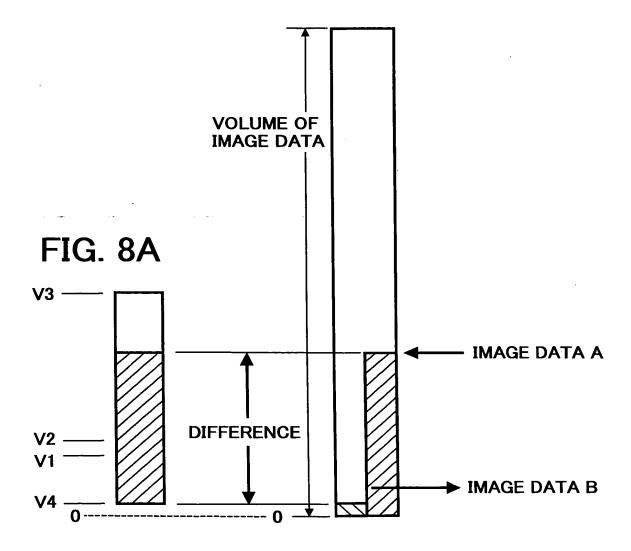
DBLON ET AL (703) 413-3000 DOCKET # 197632US-2 SHEET <u>6</u> OF <u>20</u> 605 DECOMPRESSION 유 COMPRESSION/ SECTION 604 MEMORY CONTROL SECTION IMAGE MEMORY FIG. 6 603 ပ SECTION 601 OUTPUT INPUT/ **IMAGE** OUTPUT IMAGE DATA 4..... INPUT IMAGE DATA INPUT FRAME GATE SIGNAL — OUTPUT FRAME GATE SIGNAL INPUT PIXEL SYNCHRONIZING SIGNAL OUTPUT PIXEL SYNCHRONIZING SIGNAL SYNCHRONIZING SIGNAL SYNCHRONIZING SIGNAL **OUTPUT LINE** INPUT LINE

OBLON ET AL (703) 413-3000 DOCKET # 197632US-2 SHEET <u>7</u> OF <u>20</u>

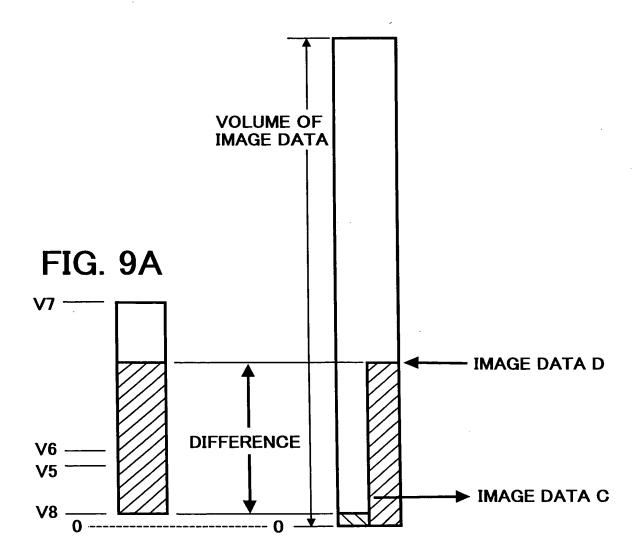
FIG. 7



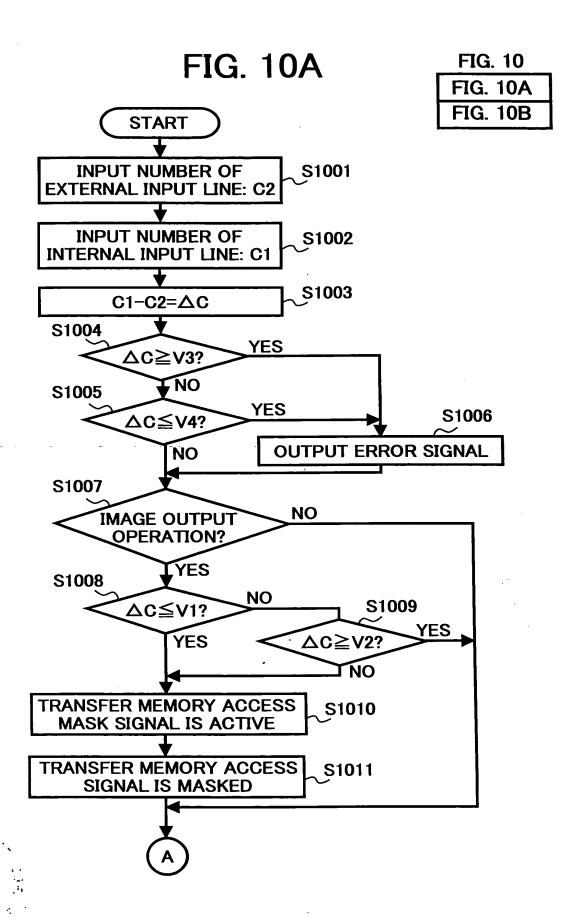








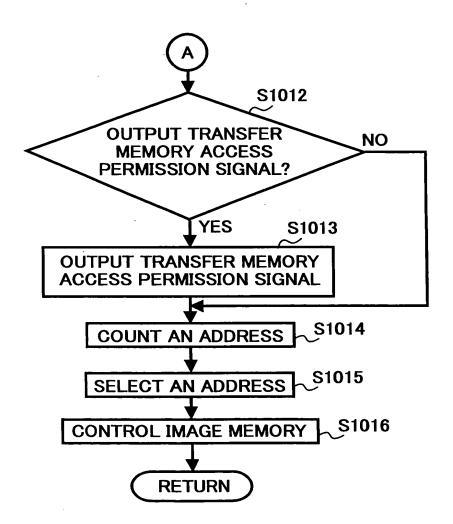


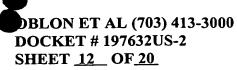


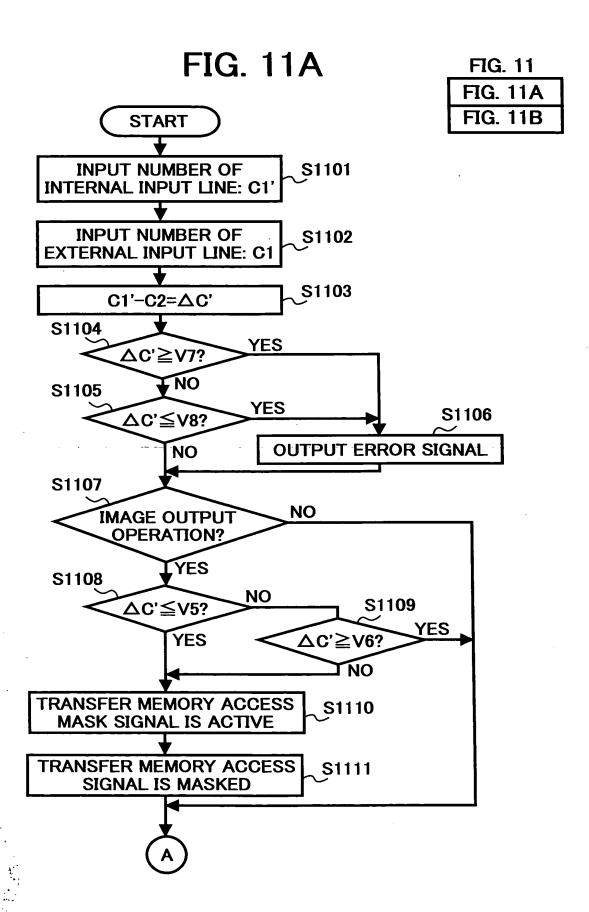
DBLON ET AL (703) 413-3000 DOCKET # 197632US-2 SHEET <u>11</u> OF <u>20</u>



FIG. 10B



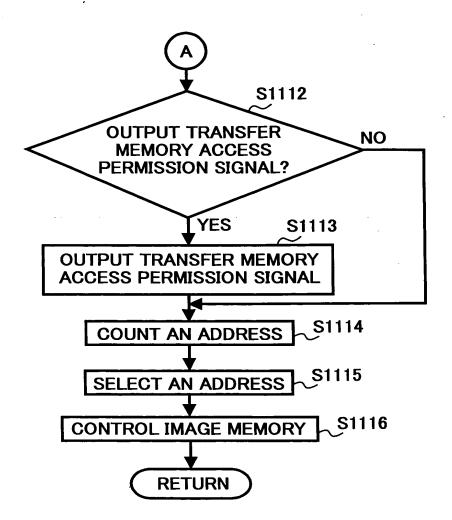






DBLON ET AL (703) 413-3000 DOCKET # 197632US-2 SHEET <u>13</u> OF <u>20</u>

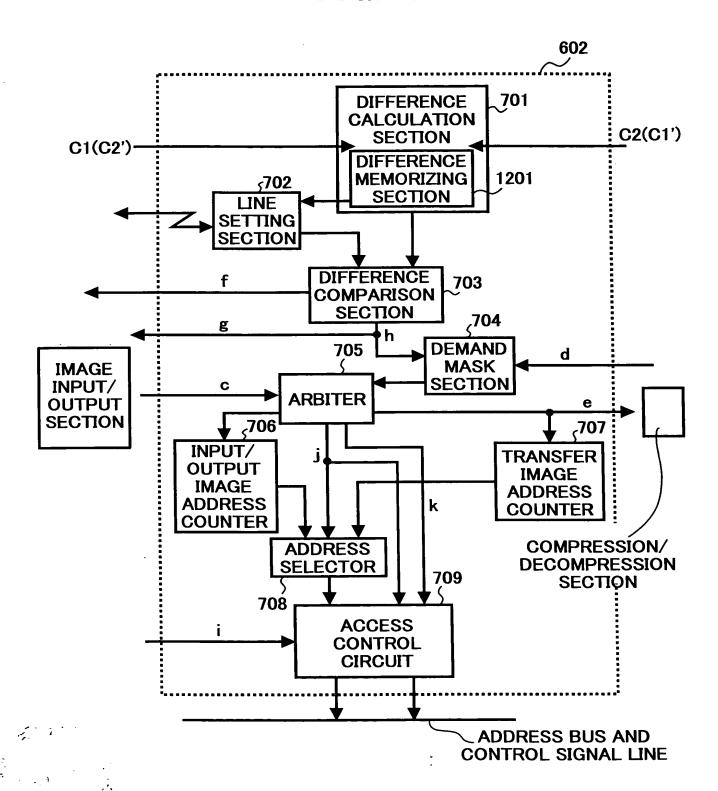
FIG. 11B

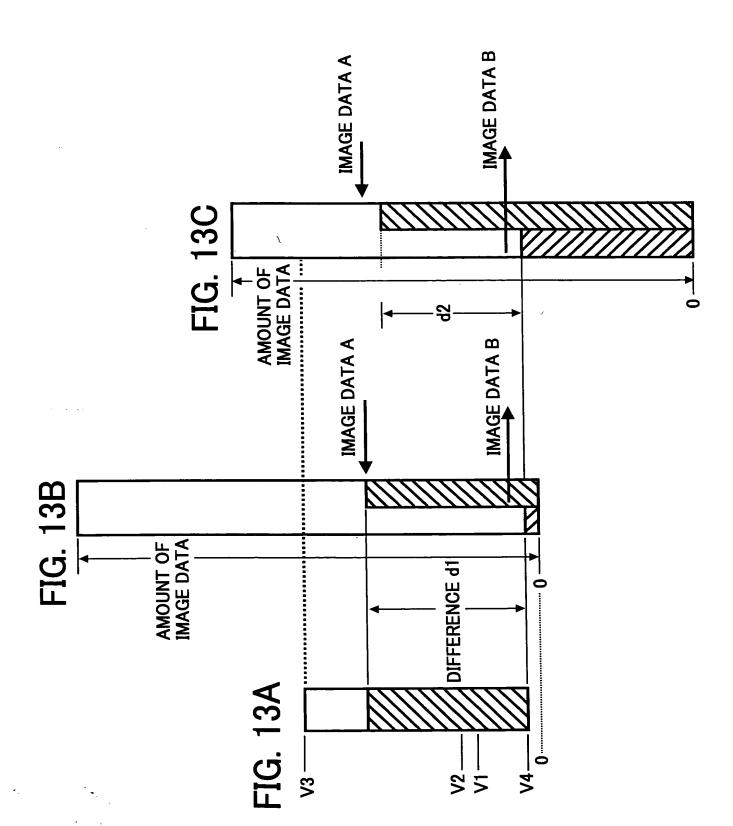


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CETTAF.

FIG. 12





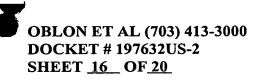
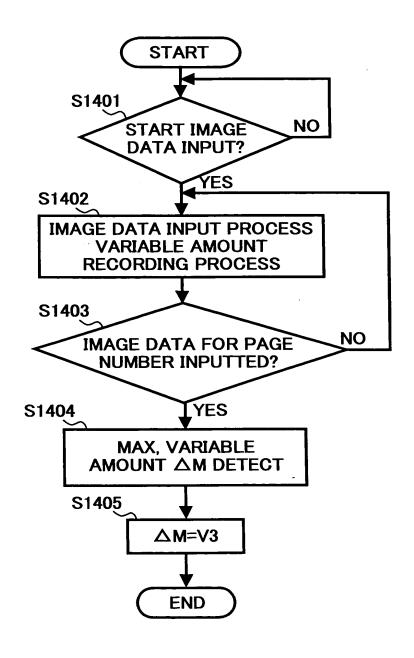
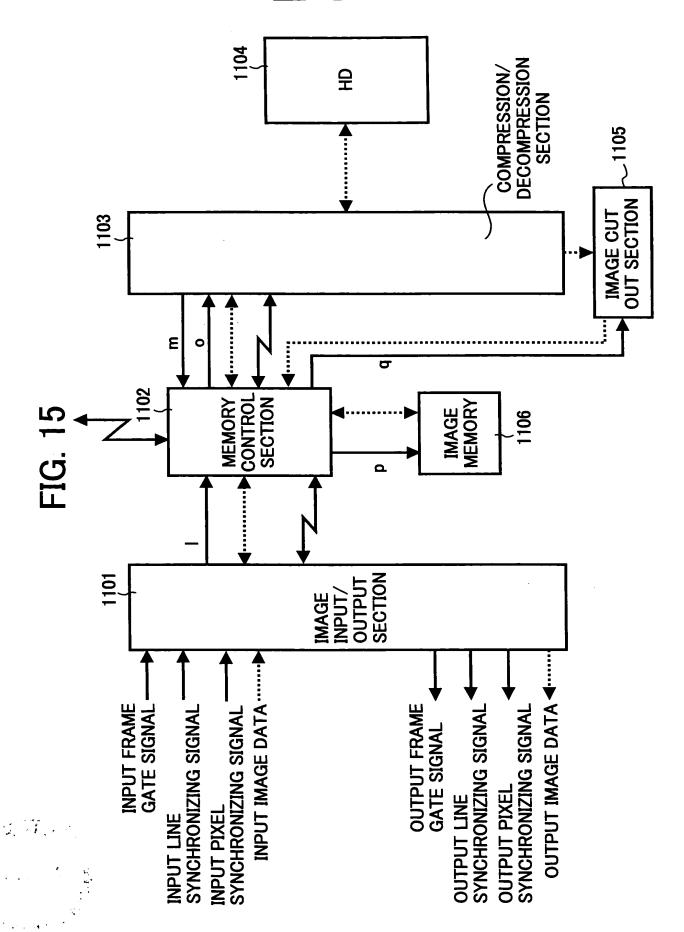


FIG. 14

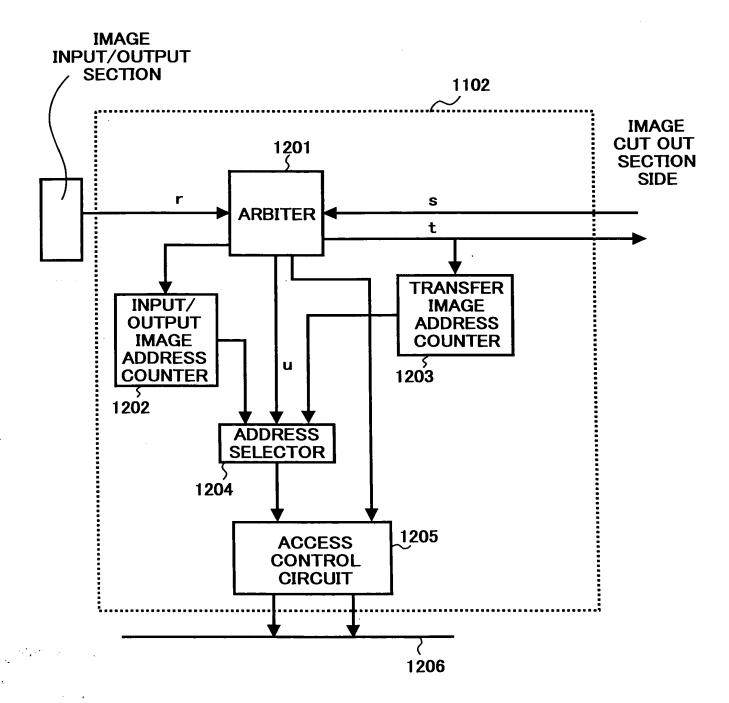






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FIG. 16





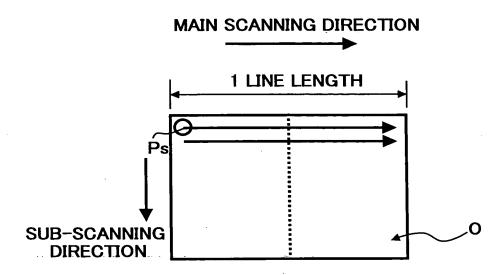
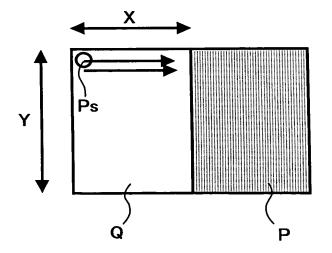


FIG. 17B



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